

Electronic Acknowledgement Receipt

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Application Number:	10604491
International Application Number:	
Confirmation Number:	1490
Title of Invention:	Multiprocessor subsystem in SoC with bridge between processor clusters interconnection and SoC system bus
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The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows: Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	FIS920030113US1_US_IDS_Form__SB_08a.pdf	686654	no	4
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8220	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			694874		
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